

Computatio, Sive, Logica: Logic

by Thomas Hobbes ; Aloysius Martinich; Isabel Payson
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An arithmetic logic unit (ALU) is a digital electronic circuit that performs arithmetic . An ALU is a fundamental building block of many types of computing circuits, and match the native word size of the encapsulating CPU (or other processor). Computational Models of Argument: Proceedings of COMMA 2012 - Google Books Result Quantum Algorithms, Paraconsistent Computation and . - Unicamp Handbook of Modal Logic - Google Books Result Hobbes conception of reason as computation or reckoning is signifi- cantly different in Part I of . Computatio sive Logica , computation, that is to say, logic. Logical Aspects of Computational Linguistics: 4th International . - Google Books Result "Computatio sive Logica" (first part: computation or logic), but did not really deliver on the . computation and logic are two names for the same thing, viz. addition. Computational Logic Course Material Philosophia rationalis sive Logica : Wolff, Christian, Freiherr von .

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